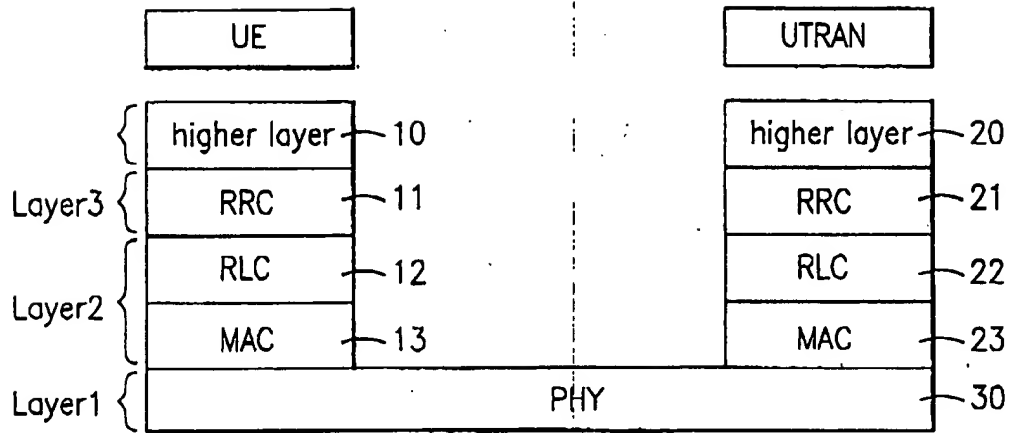


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FIG. 1



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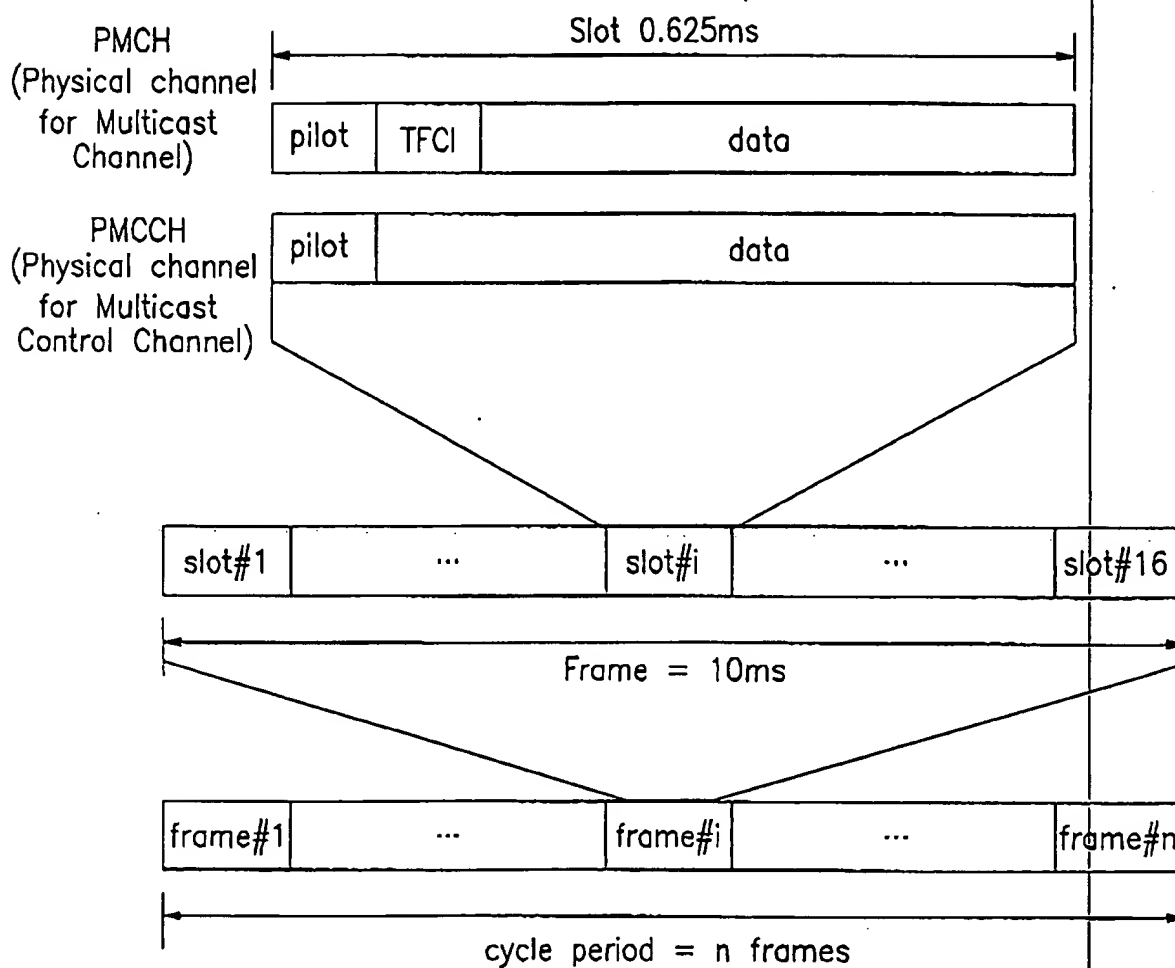


The diagram illustrates the MAC-M architecture. A central rectangular block labeled "MAC-M" contains two sub-blocks: "Scheduling/Multiplexing" and "TFC Selection". The "Scheduling/Multiplexing" block is connected to three external channels: "MAC-Control", "MTCH", and "MCCH". The "TFC Selection" block is connected to two external channels: "MTCH" and "MCH". The connections are shown as lines with ovals at the endpoints. The "MAC-M" block is connected to four external channels: "MAC-Control", "MTCH", "MCCH", and "MCH".

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~~5.4~~

FIG. 4

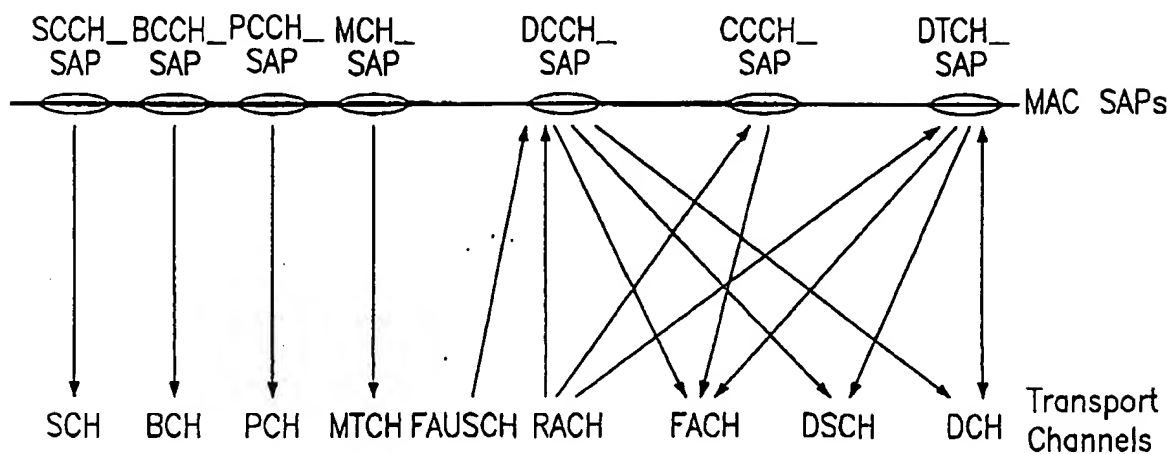


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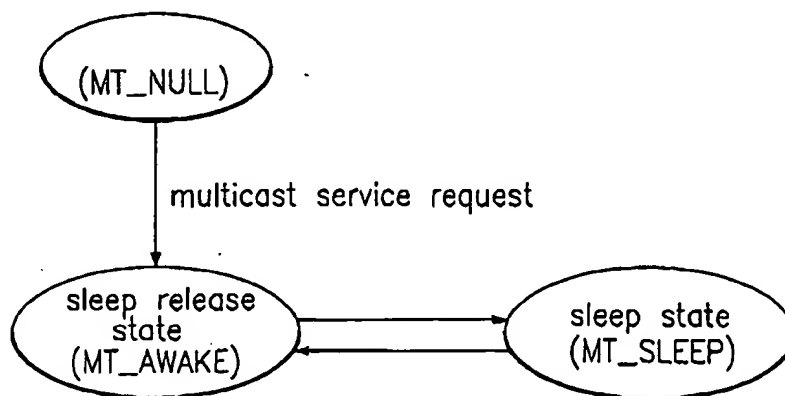
~~FIG. 5~~

FIG. 5



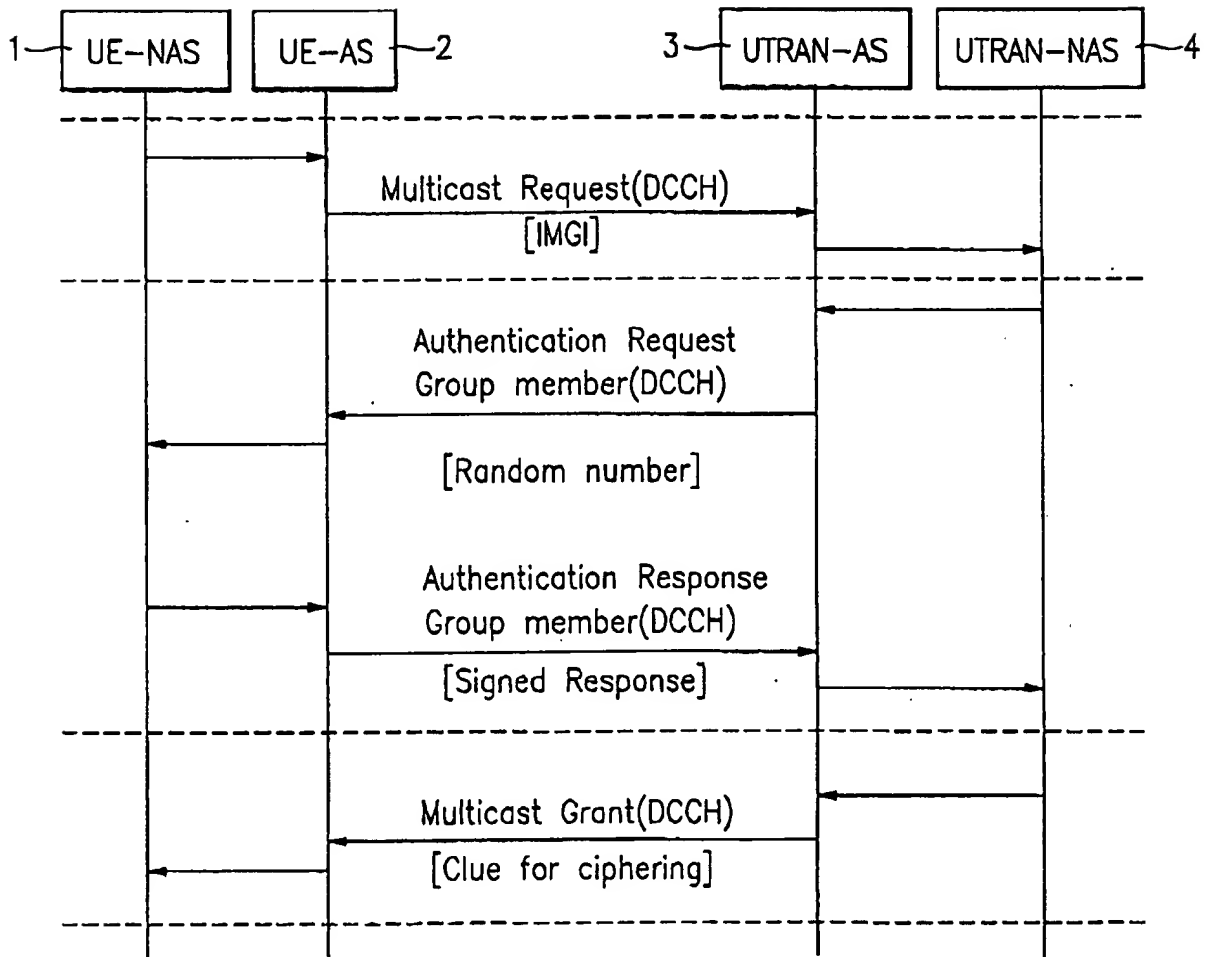
~~FIG. 6~~

FIG. 6



~~FIG. 7~~

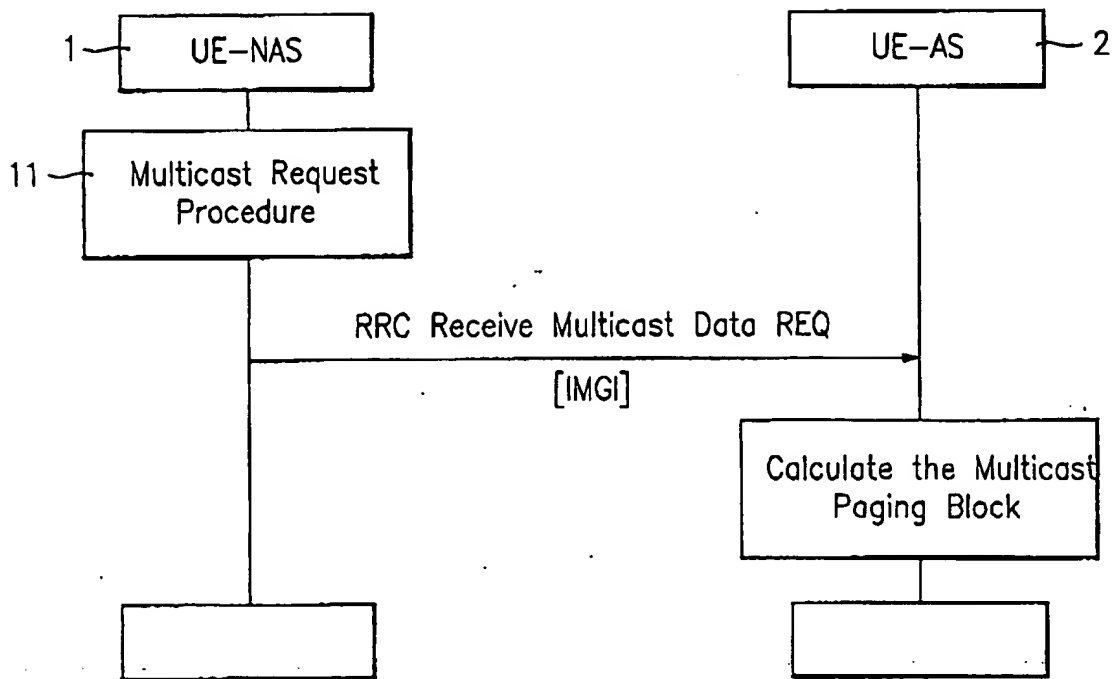
FIG. 7



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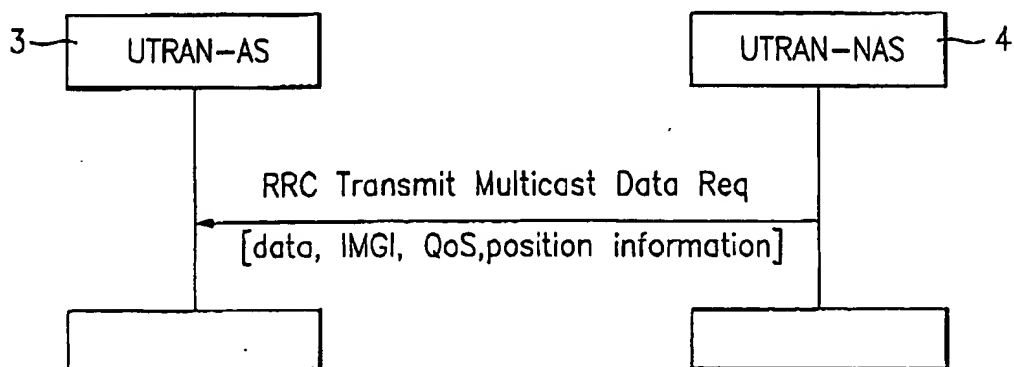
~~[5]~~ 8

FIG. 8



~~[5]~~ 9

FIG. 9

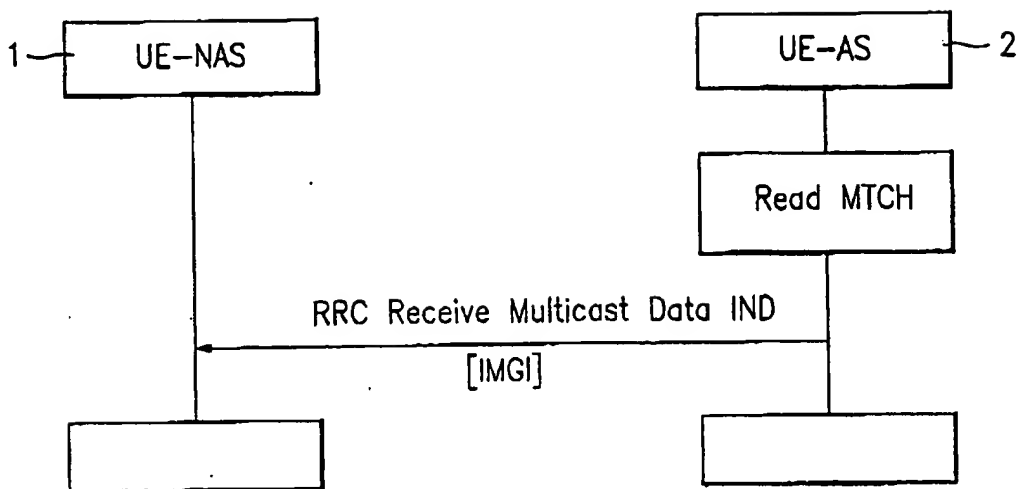


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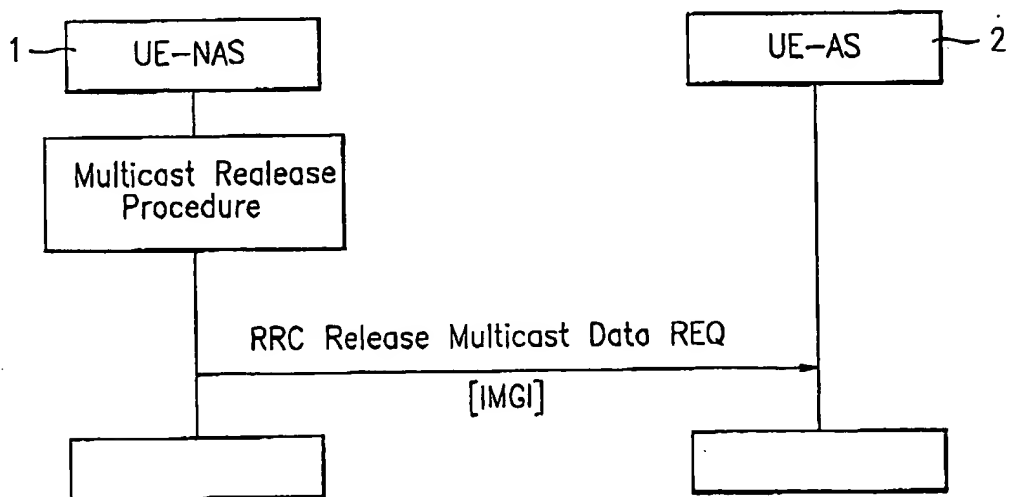
~~[E 10]~~

FIG. 10



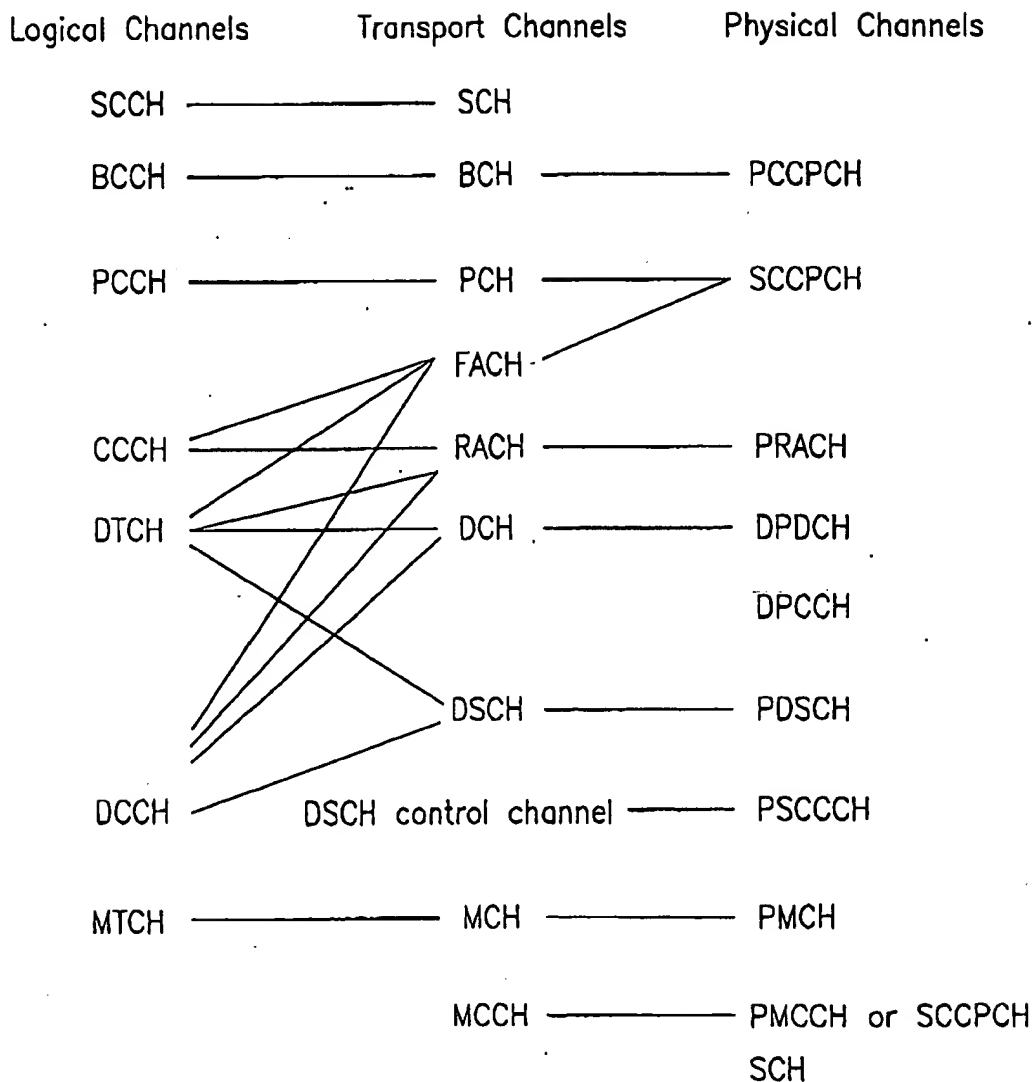
~~[E 11]~~

FIG. 11



~~[E 12]~~

FIG. 12



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